

WHAT IS CLAIMED IS:

- 1 1. An information handling system comprising:
2 a housing;
3 processing components disposed in the housing and operable to generate
4 display information;
5 a graphics component interfaced with the processing components and operable
6 to output the display information as a DVO signal;
7 a multiplexer interfaced with the graphics component to receive the DVO
8 signal and having first and second selectable outputs;
9 a first TMDS transmitter interfaced with the first selectable multiplexer output
10 and operable to transmit the DVO signal as a DVI output;
11 a first DVI connector interfaced with the first TMDS transmitter and operable
12 to provide the DVI output at the housing to an external display;
13 a second TMDS transmitter interfaced with the second selectable multiplexer
14 output and operable to transmit the DVO signal as a DVI output; and
15 a docking connector interfaced with the second TMDS transmitter and
16 operable to provide the DVI output at the housing to a docking station.
- 1 2. The information handling system of Claim 1 further comprising:
2 a docking station operable to couple to the housing and to accept the docking
3 connector; and
4 a second DVI connector interfaced with the docking connector and operable to
5 provide the DVI output at the docking station to an external display.
- 1 3. The information handling system of Claim 2 further comprising:
2 a docking station detector operable to determine insertion of the information
3 handling system into the docking station; and
4 a switch interfaced with the docking station detector and the multiplexer and
5 operable to select the first TMDS transmitter if the housing is not
6 coupled to the docking station and to select the second TMDS
7 transmitter if the housing is coupled to the docking station.

1 4. The information handling system of Claim 3 wherein the multiplexer
2 and the first and second TMDS transmitters are fabricated as an application specific
3 integrated circuit.

1 5. The information handling system of Claim 3 wherein the graphics
2 component comprises a graphics and memory controller hub.

1 6. The information handling system of Claim 3 wherein the graphics
2 component comprises a graphics processor unit.

1 7. The information handling system of Claim 3 further comprising a
2 projector operable to interface with the first DVI connector to present the display
3 information when the housing is not coupled into the docking station.

1 8. The information handling system of Claim 3 further comprising a
2 display monitor operable to interface with the second DVI connector to present the
3 display information when the housing is coupled into the docking station.

1 9. A method for presentation of display information from an information
2 handling system, the method comprising:
3 generating the display information as a DVO signal from a graphics
4 component;
5 selectively providing the DVO signal to one of a first or a second TMDS
6 transmitter;
7 transmitting display information from the first TMDS transmitter to a DVI
8 connector coupled to a housing; and
9 transmitting display information from the second TMDS transmitter to a DVI
10 connector coupled to a docking station.

1 10. The method of Claim 9 further comprising interfacing the housing DVI
2 connector to a projector.

- 1 11. The method of Claim 9 further comprising interfacing the docking
2 station DVI connector to a display monitor.
- 1 12. The method of Claim 9 wherein selectively providing the DVO signal
2 further comprises:
3 determining if the housing is coupled to the docking station;
4 selecting the first TMDS transmitter if the housing is not coupled to the
5 docking station; and
6 selecting the second TMDS transmitter if the housing is coupled to the
7 docking station.
- 1 13. The method of Claim 12 wherein determining if the housing is coupled
2 to the docking station further comprises activating a switch by the insertion or
3 removal of the housing into the docking station.
- 1 14. The method of Claim 13 wherein selectively providing the DVO signal
2 further comprises:
3 communicating the DVO signal to a multiplexer; and
4 switching the output of the DVO signal from the multiplexer to the first or
5 second TMDS transmitter based on activation of the switch by
6 insertion or removal of the housing into the docking station.
- 1 15. The method of Claim 14 wherein the graphics component comprises a
2 graphics processor unit.
- 1 16. The method of Claim 14 wherein the graphics component comprises a
2 graphics and memory controller hub.
- 1 17. A system for managing output of a DVI signal, the system comprising:
2 a multiplexer operable to accept a DVO signal having display information;
3 a first TMDS transmitter interfaced with the multiplexer and operable to
4 output the display information to a DVI connector at an information
5 handling system housing;

6 a second TMDS transmitter interfaced with the multiplexer and operable to
 7 output the display information through a docking connector to a DVI
 8 connector at a docking station; and
 9 a multiplexer output selector operable to provide the DVO signal to the first
 10 TMDS selector if the information handling system is not coupled to the
 11 docking station and further operable to provide the DVO signal to the
 12 second TMDS selector if the information handling system couples to
 13 the docking station.

1 18. The system of Claim 17 wherein the multiplexer, the first TMDS
 2 transmitter and the second TMDS transmitter are integrated into an application
 3 specific integrated circuit.

1 19. The system of Claim 17 further comprising a graphics and memory
 2 controller hub interfaced with the multiplexer and operable to output the DVO signal.

1 20. The system of Claim 17 further comprising a graphics processor unit
 2 interfaced with the multiplexer and operable to output the DVO signal.

1 21. A system for managing output of a DVI signal, the system comprising:
 2 a TMDS transmitter operable to accept a DVO signal having display
 3 information and to output the display information to a DVI connector
 4 at an information handling system housing;
 5 a multiplexer interfaced with the TMDS transmitter and operable to switch the
 6 display information output by the TMDS transmitter to a DVI
 7 connector associated with an information handling system housing or a
 8 DVI connector associated with a docking connector; and
 9 a multiplexer output selector operable to select the housing DVI connector if
 10 the information handling system is not coupled to the docking station
 11 and further operable to select the docking module connector if the
 12 information handling system couples to the docking station.

1 22. The system of Claim 21 wherein the TMDS transmitter and the
2 multiplexer are integrated in an application specific integrated circuit.